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SPECIFICATION



ESD13024337

FSP100-50NCAB

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SPECIFICATION

FSP100-50NCAB

Main Feature
Active PFC Circuit
Full Range Input

FEB 18, 2014

REV:1.1



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MODEL: FSP100-50NCAB

Revision History

<u>Rev</u>	<u>Description</u>	<u>Date</u>	<u>Author</u>
1.1	1. 5.1. TEMPERATURE RANGE Operating 0 to + 40 deg .C change to 0 to + 50 deg .C (Refer to Figure 1) 2. Add Output Power Limits vs. Ambient Temperature curve Figure 1.	2014/02/18	Stan

1. GENERAL DESCRIPTION AND SCOPE

This is the specification of Model FSP100-50NCAB; AC-line powered switching power supply with active PFC (Power Factor Correction) circuit. Designed and manufactured by FSP GROUP.

The specification below is intended to describe as detailedly as possible the functions and performance of the subject power supply. Any comment or additional requirements to this specification from our customers will be highly appreciated and treated as a new target for us to approach.

2. REFERENCE DOCUMENTS

The subject power supply will meet the EMI requirements and obtain main safety approvals as following:

2.1 EMI REGULATORY

- FCC Part 15 Subpart J, Class 'B' 115 Vac operation.
- CISPR 22 Class 'B' 230 Vac operation.

3. PHYSICAL REQUIREMENTS

3.1 MECHANICAL SPECIFICATIONS

The mechanical drawing of the subject power supply, which indicate the form factor, location of the mounting holes, location, the length of the connectors, and other physical specifications of the subject power supply. Please refer to the attachment drawing.

4. ELECTRICAL REQUIREMENTS

4.1 OUTPUT ELECTRICAL REQUIREMENTS

The subject power supply will meet all electrical specifications below, over the full operation temperature range and dynamic load regulation.

4.1.1. OUTPUT RATING

Output	Nominal	Regulation	Ripple/Noise	Min	Max
1	+3.3V	±5%	75mV	0.2A	8A
2	+5V	±5%	75mV	0.5A	10 A
3	+12V	±5%	150mV	1.0A	7.5A
4	-12V	±10%	150mV	0.0 A	0.5A
5	+5VSB	±5%	75mV	0.0 A	2.0A

The +3.3V, +5V total output shall not exceed 50 watts and the total output for this subject power supply is 100 watts. Ripple and noise measurements shall be made under all specified load conditions through a single pole low pass filter with 20MHz cutoff frequency. Outputs shall be bypassed at the connector with a 0.1uF ceramic disk capacitor and a 10uF electrolytic capacitor to simulate system loading.

4.1.2. LOAD CAPACITY SPECIFICATIONS

The cross regulation defined as follows, the voltage regulation limits DC include DC Output ripple & noise.

100W

LOAD	STM	+3.3V	+5V	+12V	-12V	+5Vsb
ALL MAX	HHHH	6.0A	6.0A	3.5A	0.3A	1.0A
+5V MAX other MIN	LHLL	0.3A	10A	1.0A	0A	0A
+3.3V MAX other MIN	HLLL	8.0A	0.5A	1.0A	0A	0A
+12V MAX other MIN	LLHL	0.3 A	0.5A	7.5A	0A	0A
ALL MIN	LLLL	0.3 A	0.5A	1.0A	0A	0A

4.1.3. HOLD-UP TIME (@80% LOAD)

115V / 60Hz : 16 mSec. Minimum.

4.1.4.OUTPUT RISE TIME

(10% TO 90% OF FINAL OUTPUT VALUE, @FULL LOAD)

115V-rms or 230V-rms + 5Vdc : 20ms Maximum

4.1.5.OVER VOLTAGE PROTECTION

+5V_{dc} output: + 6.82V_{dc} maximum
 +12V_{dc} output: +16.5V_{dc} maximum
 + 3.3V_{dc} output: + 4.5V_{dc} maximum

4.1.6.SHORT CIRCUIT PROTECTION

Output short circuit is defined to be a short circuit load of less than 0.1 ohm.

In the event of an output short circuit condition on +3.3V, +5V or +12V output, the power supply will shutdown and latch off. The power supply shall return to normal operation after the short circuit has been removed and the power switch has been turned off for no more than 2 seconds.

In the event of an output short circuit condition –12V output, the power supply will not be latch off. The power supply shall return to normal operation as soon as the short circuit has been removed and the power switch has been turned off for no more than 2 seconds.

4.1.7.POWER GOOD SIGNAL

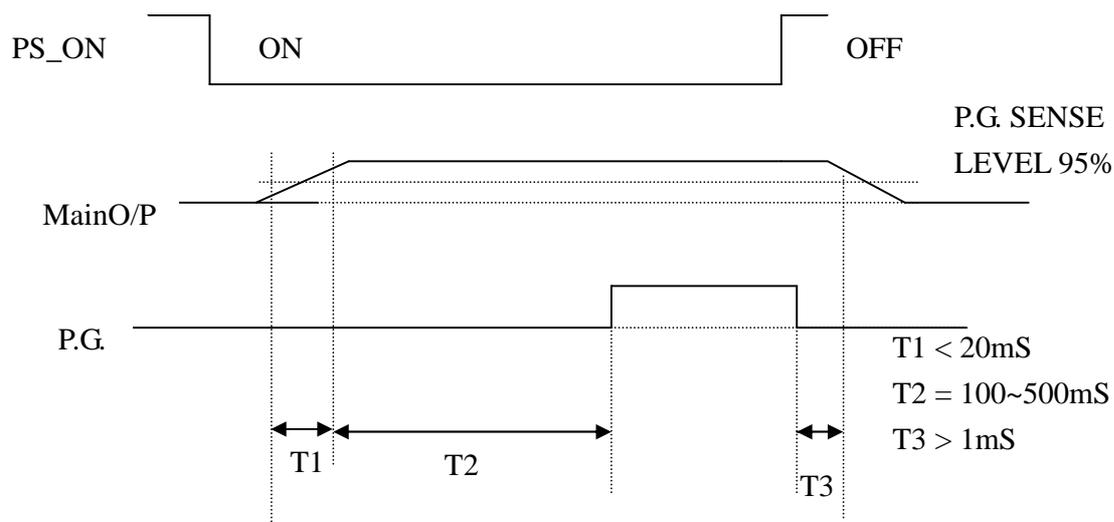
The power good signal is a TTL compatible signal for the purpose of initiating an orderly star-up procedure under normal input operating conditions. This signal is asserted (low) until +5Vdc has reached 4.75 volts during power up. Characteristics:

TTL signal asserted (low state) : less than 0.5V while sinking 10mA.

TTL signal asserted (high state): greater than 4.75V while sourcing 500uA.

High state output impedance: less or equal to 1Kohm from output to common.

POWER GOOD @ 115/230V,FULL LOAD	100 –500mSec.
POWER FAIL @115/230V, FULL LOAD	1 mSec. minimum



4.2. OUTPUT TRANSIENT LOAD RESPONSE

+5V and +12V must be within specification for a step change in current as specified below. The outputs will be tested one section at a time with all other sections at maximum load. The test transition will be from IA to IB and IB to IA.

On TRANSIENT test, power good signal should be take with ch4.1.8.

+5Vdc:

IA:	10 amps
IB:	5.0 amps
Volts variation:	+5V +-5%
Setting time:	10 ms max
Transient load slew rate is	0.5A/ μ S

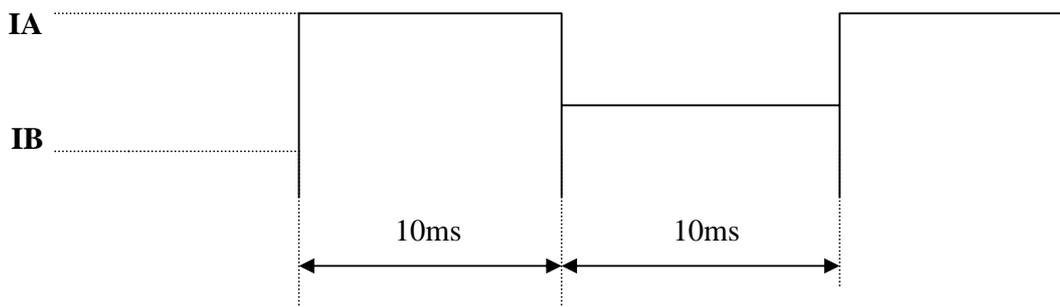
+12Vdc:

IA:	7.5 amps
IB:	3.75 amps
Volts variation:	+12V +-5%
Setting time:	10 ms max
Transient load slew rate is	0.1A/ μ S

+3.3Vdc:

IA:	8.0 amps
IB:	4.0 amps
Volts variation:	+3.3V +-5%
Setting time:	10 ms max

Transient load slew rate is 0.1A/μS



4.3. INPUT ELECTRICAL SPECIFICATIONS

4.3.1. VOLTAGE RANGE

	PARAMETER	UNITS
V-in Range	90 - 264	V-rms

4.3.2. INPUT FREQUENCY

INPUT FREQUENCY	47-63Hz
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4.3.3. INRUSH CURRENT

(Cold start – 25 deg. C)

115V	165A
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4.3.4. INPUT LINE CURRENT

115V	3.0 Amps – rms maximum
230V	1.5 Amps – rms maximum

4.3.5 Efficiency

80% minimum under full load and 115 Vrms input .

4.4. PS_ ON#

PS_ON# is an active-low, TTL-compatible signal that allows a motherboard to remotely control the power supply in conjunction with features such as soft on/off, Wake on LAN+, or wake-on-modem. When PS_ON# is pulled to TTL low, the power supply should turn on the five main DC output rails: +12VDC,+5VDC,+3.3VDC and –12VDC. When PS_ON# is pulled to TTL high or open-circuited, the DC output rails should not deliver current and should be held at zero potential with respect to ground. PS_ON# has no effect on the +5VSB output, which is always enabled whenever

the AC power is present. (PS_ON# Signal Characteristics)

The power supply shall provide an internal pull-up to TTL high. The power supply shall also provide debounce circuitry on PS_ON# to prevent it from oscillating on/off at startup when activated by a mechanical switch. The DC output enable circuitry must be SELV-compliant.

PS_ON# Signal Characteristics

	Min.	Max.
VIL, Input Low Voltage	0.0V	0.8V
IIL, Input Low Current (Vin = 0.4V)		-1.6mA
VIH, Input High Voltage (Iin = -200 μ A)	2.0V	
VIH OPEN circuit, Iin = 0		5.25V

5. ENVIRONMENTAL REQUIREMENTS

The power supply will be compliant with each item in this specification for the following Environmental conditions.

5.1. TEMPERATURE RANGE

Operating	0 to + 50 deg. C (Refer to Figure 1)
Storage	-20 to +80 deg. C

Output Power Limits vs. Ambient Temperature

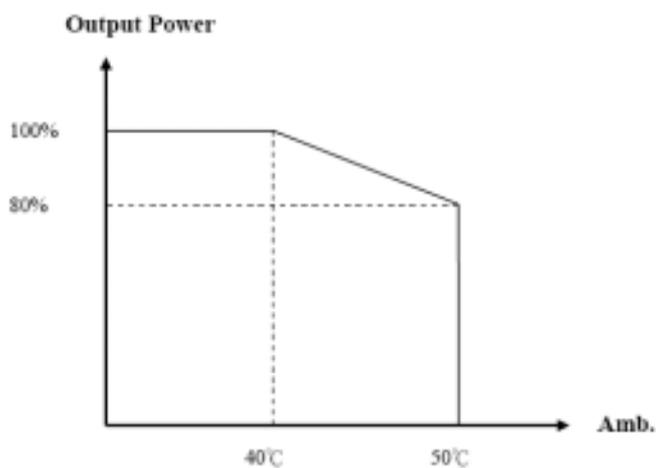


Figure 1

5.2. HUMIDITY

Operating	90% RH, Non-condensing
Storage	95% RH, Non-condensing

6. SAFETY

6.1. LEAKAGE CURRENT

The leakage current from AC to safety ground will not exceed 3.5 mA-rms at 264Vac, 50 Hz.

7. ELECTROMAGNETIC COMPATIBILITY

7.1 LINE CONDUCTED EMI

The subject power supplies will meet FCC .

7.2. RADIATED EMI

The subject power supplies will meet FCC and CISPR 22 requirements .

8. LABELLING

Label marking will be permanent, legible and complied with all agency requirements.

8.1. MODEL NUMBER LABEL

Labels will be affixed to the sides of the power supply showing the following:

- Manufacturer's name and logo.
- Model no., serial no., revision level, location of manufacturer.
- The total power output and the maximum load for each output.
- AC input rating.

8.2 DC OUTPUT IDENTIFICATION

Each output connector will be labeled.

9. RELIABILITY

9.1. MTBF

The power supply have a minimum predicted MTBF(MIL-HDBK-217) of 100,000 hours of continuous operation at 25 , maximum-output load, and nominal AC input voltage.